

# Vlsi Electronics: Microstructure Science Volume 8 : Plasma Processing For VLSI

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An integrated circuit arrangement having at least two components has in a substrate, an insulation structure (4', 5) between the components which covers at least one

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opening formed in the patterned resist. The etching preferably comprises dry etching within a dual source, high density plasma etcher using an oxygen containing gas.

Volume 8 (2010) Open Access Issue 6 S.P. Murarka, Silicides for VLSI Applications, Academic 1983. [31] R.N. Singh, VLSI Electronics: Microstructure Science

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