

CMOS Analog Integrated Circuits: High-Speed And Power-Efficient Design By Tertulien Ndjountche

By Tertulien Ndjountche

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Specific structures or topologies for the analog multiplier that have high speed, CMOS analog multiplier circuits analog and digital integrated circuit

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Integrated Circuit TTL High Speed CMOS, Description: The NTE74HC4053 is a digitally controlled analog switch in a 16 Lead DIP type package that utilizes

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A simplified sample and hold circuit diagram. AI is an analog input, Ndjountche, Tertulien (2011). CMOS Analog Integrated Circuits: High-Speed and Power-Efficient

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