

# Analog Design And Simulation Using OrCAD Capture And PSpice By Dennis Fitzpatrick

**By Dennis Fitzpatrick**

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Design and Simulation using OrCAD Capture and PSpice provides step-by-step instructions on how to use the Cadence/OrCAD family of Electronic Design Automation

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You can group digital signals together and display them as a bus in the Probe window. The bus name can be created in the Trace Expression field in the Trace > Add

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Author(s) Fitzpatrick, Dennis: Publication Amsterdam : Elsevier, 2012. - 329 p. Subject code 621.382.049.75: Subject category Engineering: Keywords electronic circuit

This book is an introductory text for those wishing to learn about analog design and verification. While it uses OrCad tools, it provides a firm

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This blog post is a condensation of a series of posting on EDN about the book Analog Design and Simulation using OrCAD Capture and PSpice, written by Dennis

ELECTRONIC CIRCUIT SIMULATION TOOLS USING Analog Design and Simulation using OrCAD Capture and PSPICE Lee. Seung C. OrCAD PSPICE with circuit analysis.

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